10/31/23

Griffin Rzonca

ECE 445

Meeting with Jason

* Audio board done - needs verification from Jason
* Power board testing as we wait for audio PCB to arrive
* Mock demos begin week of 11/13 - anything we need to prep?
  + Should be mostly assembled, does not need to be completely finished
* USB/UART Bridge, 5V to 3.3V voltage regulators in ECEB?
* I need Power lab access
* Initial post score
  + Jason will check on this and add points
* Once you get it working, TAKE VIDEO
* Should be nearly done before mock demo
* Worry about switches
* Large loop from gate drivers, may add a lot of inductance
* Mock Demo
  + Print out 3 copies of Block diagram, HL reqs, R&V tables
  + Prof and TAs will ask questions
* USB/UART Bridge:
  + Jason can check out USB/UART bridge for us
* 5V to 3.3V Regulators
  + Look at what the shop has in stock, Jason can get us one
  + T0252 Voltage regulator footprint
  + Look at SMD component inventory in E Shop
  + SOT23 is the transistor footprint
* I need Power lab access
  + Email Jason to remind him to email Joyce to ask for power lab access
  + We’re 445 group, we need power lab access, what are the rules governing